

AMENDMENTS TO THE CLAIMS

Please cancel claims 4, 6 and 11-12, amend claims 1, 5 and 7, and add claims 13-16 as indicated:

1. (currently amended) A data recording and reproducing apparatus comprising:
  - a random-accessible recording device for storing data;
  - an input path for transferring input data to said recording device;
  - an input buffer disposed on said input path for temporarily storing said input data;
  - an output path for transferring output data stored in said recording device, said output path being separate from said input path;
  - an output buffer for temporarily storing the output data transferred through said output path; and
  - a controller for simultaneously storing said input data into said input buffer and transferring said output data from said output buffer, in parallel with writing from said input buffer to said recording device or reading from said recording device to said output buffer, wherein each of said input buffer and said output buffer has a first memory area and a second memory area, and  $t_1 + t_2 > T_1$  and  $t_1 + t_2 > T_2$  are satisfied where  $t_1$  is a time required for writing data into said recording device the amount of which is sufficient to fill the first or second memory area of said input buffer,  $T_1$  is a time required for filling the first or second memory area of said input buffer with data,  $t_2$  is a time required for reading out data from said recording device the amount of which is sufficient to fill the first or second memory area of said output buffer, and  $T_2$  is a time required for completely outputting the data filled in the first or second memory area of said output buffer.
2. (original) The data recording and reproducing apparatus according to claim 1, wherein said controller is positioned between said input and output buffers and said recording device and performing said storing, said transferring, said writing, and said reading, in response to commands from the outside.

3. (original) The data recording and reproducing apparatus according to claim 2, wherein a path used for command input and status output is connected to said controller, separately from said input path and said output path.

4. (cancelled)

5. (currently amended) A method for recording and reproducing video data in which the video data is recorded in a random-accessible recording device having separate input and output buffers and the video data recorded in said recording device is reproduced, said method comprising the steps of:

storing the video data from the outside into said input buffer;

writing the video data stored in said input buffer to said recording device;

reading out the video data recorded in said recording device to said output buffer; and

transferring the video data read out to said output buffer to the outside simultaneously with said storing step, wherein each of said input buffer and said output buffer has a first memory area and a second memory area, and  $t_1 + t_2 > T_1$  and  $t_1 + t_2 > T_2$  are satisfied where  $t_1$  is a time required for writing data into said recording device the amount of which is sufficient to fill the first or second memory area of said input buffer,  $T_1$  is a time required for filling the first or second memory area of said input buffer with data,  $t_2$  is a time required for reading out data from said recording device the amount of which is sufficient to fill the first or second memory area of said output buffer, and  $T_2$  is a time required for completely outputting the data filled in the first or second memory area of said output buffer.

6. (cancelled)

7. (currently amended) A disk drive unit comprising:

a hard disk for storing data;

an input path for transferring input data to said hard disk;

an output path for outputting data stored in said hard disk, said output path being separate from said input path; and

a file system disposed between said input and output paths and said hard disk for managing data stored in said hard disk, wherein each of said input buffer and said output buffer has a first memory area and a second memory area, and  $t_1 + t_2 > T_1$  and  $t_1 + t_2 > T_2$  are satisfied where  $t_1$  is a time required for writing data into said hard disk the amount of which is sufficient to fill the first or second memory area of said input buffer,  $T_1$  is a time required for filling the first or second memory area of said input buffer with data,  $t_2$  is a time required for reading out data from said hard disk the amount of which is sufficient to fill the first or second memory area of said output buffer, and  $T_2$  is a time required for completely outputting the data filled in the first or second memory area of said output buffer.

8. (original) The disk drive unit according to claim 7, wherein an input buffer is disposed on said input path, an output buffer is disposed on said output path, and a controller for controlling said input buffer and said output buffer is provided between said input and output buffers and said hard disk.

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9. (original) The disk drive unit according to claim 8, wherein said file system is built in said controller.

10. (original) The disk drive unit according to claim 9, wherein said controller stores data into said input buffer and transfers data from said output buffer simultaneously, in parallel with writing from said input buffer to said hard disk or reading from said hard disk to said output buffer.

11. (cancelled)

12. (cancelled)

13. (new) A data recording and reproducing apparatus comprising:

- a random-accessible recording device for storing data;
- an input path for transferring input data to said recording device;
- an input buffer disposed on said input path for temporarily storing said input data;

an output path for transferring output data stored in said recording device, said output path being separate from said input path;

an output buffer for temporarily storing the output data transferred through said output path; and

a controller for simultaneously storing said input data into said input buffer and transferring said output data from said output buffer, in parallel with writing from said input buffer to said recording device or reading from said recording device to said output buffer, wherein said operations of writing to and reading from said recording device are performed by a single read/write head using data supplied from and to said respective input and output buffers.

14. (new) The data recording and reproducing apparatus according to claim 13, wherein said controller is positioned between said input and output buffers and said recording device and performing said storing, said transferring, said writing, and said reading, in response to commands from the outside.

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15. (new) The data recording and reproducing apparatus according to claim 14, wherein a path used for command input and status output is connected to said controller, separately from said input path and said output path.

16. (new) The data recording and reproducing apparatus according to claim 13, wherein each of said input buffer and said output buffer has a first memory area and a second memory area, and  $t_1 + t_2 > T_1$  and  $t_1 + t_2 > T_2$  are satisfied where  $t_1$  is a time required for writing data into said recording device the amount of which is sufficient to fill the first or second memory area of said input buffer,  $T_1$  is a time required for filling the first or second memory area of said input buffer with data,  $t_2$  is a time required for reading out data from said recording device the amount of which is sufficient to fill the first or second memory area of said output buffer, and  $T_2$  is a time required for completely outputting the data filled in the first or second memory area of said output buffer.